

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Makoto YOSHIDA et al.

Serial No.: Not Yet Assigned
(Continuation of Serial No. 10/227,799, filed August 27, 2002)

Filed: Even date herewith

For: **SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND
MANUFACTURING METHOD THEREOF**

Group: 2815

Examiner: Edward Joseph Wojciechowicz

CLAIM FOR PRIORITY

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

September 4, 2003

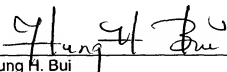
Sir:

Pursuant to 35 U.S.C. §119 and 37 CFR 1.55, the applicants hereby claim the right of priority based on the following foreign application filed in Japan:

Application No. 9-164766 filed on June 20, 1997.

A certified copy of this foreign application was filed on April 14, 2000, in application Serial No. 09/446,302, the grandparent application of the present continuation application.

Respectfully submitted,
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